INTERNAL STRUCTURE OPTIMIZATION OF A DAR IMPATT DIODE FOR HIGH FREQUENCY BAND

Alexander Zemliak¹, Santiago Cabrera¹, Georgina Beltran¹, Vladimir Golovin²

¹Puebla Autonomous University, ²National Technical University of Ukraine

azemliak@fcfm.buap.mx, scam@ece.buap.mx, gbeltran@fcfm.buap.mx, gol@rtus.ntu-kpi.kiev.ua

ABSTRACT

The analysis and optimization of the $n^+pvnp^+$ avalanche diode have been realized on basis of the nonlinear model and special optimization procedure. This type of the diode that was named as Double Avalanche Region (DAR) IMPATT diode includes two avalanche regions inside the diode. The admittance and energy characteristics of the DAR diode were analyzed in very wide frequency band from 30 up to 360 GHz. Output power level was optimized for the second frequency band near the 220 GHz.

1. INTRODUCTION

One of the important problems of modern microwave electronics concerns of the power generation in millimeter region. The IMPATT diodes of different structures are used very frequently in microwave systems. The single drift region (SDR) and the double drift region (DDR) IMPATT diodes are very well known and used successfully for the microwave power generation [1, 2]. From the famous paper of Read [3] the main idea to obtain the negative resistance was defined on the basis of the phase difference being produced between RF voltage and RF current due to delay in the avalanche build-up process and the transit time of charge carriers. The transit time delay of both types of diodes (SDR and DDR) is the essential factor of the necessary phase conditions to obtain negative resistance. However an IMPATT diode that has double avalanche regions (DAR) can produce an avalanche delay which alone can satisfy conditions necessary to generate microwave power [4-6]. The DAR diode can be defined for instance by means of the structure $n^+pvnp^+$ in Fig. 1. This diode has two avalanche regions around $n^+p$ and $np^+$ junctions and one common drift region. This type of diode was suggested in [4]. The frequency characteristics of this diode were analyzed in [5-6] by means of the approximate model. The authors affirm that the diode active properties are produced in many frequency bands for any drift zone width. Our preliminary analysis obtained on basis of the sufficiently precise model [7] contradicts to the results [6]. We suppose that it is necessary to analyze the DAR diode carefully to obtain the optimal characteristics.

2. NONLINEAR MODEL

The drift-diffusion model which is used for the diode analysis consists of two continuity equations for the electrons and holes, the Poisson equation for the potential distribution in semiconductor structure and necessary boundary conditions as for continuity equations and for the Poisson equation. The principal equations can be presented in next form:

$$\frac{\partial n(x,t)}{\partial t} = \frac{\partial J_n(x,t)}{\partial x} + \alpha_n J_n(x,t) + \alpha_p J_p(x,t)$$

$$\frac{\partial p(x,t)}{\partial t} = -\frac{\partial J_p(x,t)}{\partial x} + \alpha_n J_n(x,t) + \alpha_p J_p(x,t)$$

$$J_n(x,t) = n(x,t) V_n + D_n \frac{\partial n(x,t)}{\partial x}$$

$$J_p(x,t) = p(x,t) V_p - D_p \frac{\partial p(x,t)}{\partial x}$$

where $n, p$ are the concentrations of electrons and holes; $J_n, J_p$ are the current densities; $\alpha_n, \alpha_p$ are the ionization coefficients; $V_n, V_p$ are the drift velocities; $D_n, D_p$ are the diffusion coefficients. The dependences of the ionization coefficients on field and temperature
and charge transport properties have been approximated using the approach in [8-9].

The boundary conditions for this system include concentration and current definition for contact points and can be written as follows:

\[
\begin{align*}
    n(0,t) &= N_D(0), & p(0,t) &= N_A(l_0), & J_n(l_0,t) &= J_m, & J_p(l_0,t) &= J_m,
\end{align*}
\]

where \( J_{nx} \), \( J_{px} \) are the electron current and the hole current for inversely biased \( p-n \) junction; \( N_D(0), N_A(l_0) \) are the concentrations of donors and acceptors at two end space points \( x = 0 \) and \( x = l_0 \); where \( l_0 \) is the length of the active layer of semiconductor structure.

The electrical field distribution in semiconductor structure can be obtained from the Poisson equation. As electron and hole concentrations are functions of the time, therefore, this equation is the time dependent too and time is the equation parameter. The Poisson equation for the above defined problem has the following normalized form:

\[
\frac{\partial^2 U(x,t)}{\partial x^2} = \frac{\partial^2 U(x,t)}{\partial t^2} = N_D(x) - N_A(x) + j(x,t) - n(x,t)
\]

where \( N_D(x), N_A(x) \) are the concentrations of donors and acceptors accordingly, \( U(x,t) \) is the potential, \( E(x,t) \) is the electrical field. The boundary conditions for this equation are:

\[
U(0,t) = 0; \quad U(l_0,t) = U_0 + \sum_{m=1}^{M} U_m \sin(\omega_m t + \varphi_m)
\]

where \( U_0 \) is the DC voltage on diode contacts; \( U_m \) is the amplitude of harmonic number \( m \) in diode contacts; \( \omega \) is the fundamental frequency; \( \varphi_m \) is the phase of harmonic number \( m \); \( M \) is the number of harmonics. In this paper we analyze one harmonic regime only \( (M=1) \) and in this case the phase \( \varphi_m \) can be define as 0. Concrete values of the voltages \( U_0, U_1 \) and frequency \( \omega \) have been defined during the analysis in section 4. Equations (1)-(4) adequately describe processes in the IMPATT diode in a wide frequency band. However, numerical solution of this system of equations is very difficult due to existing of a sharp dependence of equation coefficients on electric field. Explicit numerical schemes have poor stability and require a lot of computing time for good calculation accuracy obtaining. It is more advantageous to use implicit numerical scheme that has a significant property of absolute stability [10]. Computational efficiency and numerical algorithm accuracy are improved by applying the space and the time coordinates symmetric approximation.

After approximation of functions and its differentials the system (1) is transformed to the implicit modified Crank-Nicholson numerical scheme [10]. This modification consists of two numerical systems each of them having three-diagonal matrix.

Numerical algorithm for the calculation of IMPATT diode characteristics consists of the following stages: 1) the voltage is calculated at the diode contacts for every time step by formula (4); 2) the voltage distribution is calculated at every space point from the Poisson equation (3) by factorization method, the electrical field distribution along the diode active layer is calculated; 3) the charge carriers ionization and drift parameters are calculated in numerical net nodes for the current time step; 4) the system of equations (1) is solved by matrix factorization method taking into account the boundary conditions (2) and electron and hole concentration distributions are calculated for the current time step and than the calculation cycle is repeated for all time steps until the end of the time period; 5) the full current in external circuit is calculated taking into account a series resistance \( R_s \) \( (R_s = 0.5 \Omega \cdot \text{Ohm} \cdot 6 \text{Ohm} \cdot \text{cm}^2) \), which reflects losses in semiconductor structure. This process is continued from one period to another until the convergence is achieved by means of the results comparison for the two neighboring periods with the necessary precision. Then all harmonics of the external current, admittance for the harmonic number \( m \) and power characteristics can be calculated by the Fourier transformation.

The special optimization algorithm that combines one kind of direct method and a gradient method was used to optimize the output characteristics of DAR diode. To obtain the better solution for the optimum procedure, it is necessary to analyze \( N \)-dimensional space for \( N=5 \). The principal vector of optimization parameters consists of five variables \( y = (y_1, y_2, y_3, y_4, y_5) \), where the components will be defined below.

3. Numerical Scheme Convergence

The numerical scheme for the problem (1) for the DDR IMPATT diode structures was produced some years ago [10]. The scheme analysis showed a very good convergence of the numerical model. The numerical algorithm convergence was obtained during 6–8 high frequency periods. The careful analysis of numerical model for the DAR diode with the doping profile in Fig.1 shows that the numerical scheme convergence for this type of the doping profile is very slow and the numerical transition process continues many periods to obtain the stationary mode.

The quantitative results of the numerical scheme convergence for the principal diode characteristic, DAR diode conductance as the period number function are shown in Fig. 2. The necessary number of the consequent periods depends on the diode width and operating frequency. Results in Fig. 2 correspond to the
optimal diode width that was analyzed in section 4. The necessary period number changes from 30 – 50 for the frequency band 15 – 60 GHz up to 150 – 250 periods for 200 – 300 GHz. This very slow convergence was stipulated by the asynchronies movement of the electron and hole avalanches along the same drift region. It occurs owing to the different drift velocities of the carriers. This effect provokes a large number of necessary periods as we can see from Fig. 2 and large computer time. This is a specific feature of the analyzed type of diode structure.

4. NUMERICAL RESULTS

The accurate analysis for DAR IMPATT diode has been made for different values of \( p, n \) and \( v \) region width and the different donor and acceptor concentration level. The values of \( U_0 \) and \( U_1 \) have been changed from 25 to 27 V and from 0.1 to 12 V respectively. The same doping profile as in [6] gives the negative conductance for very narrow frequency band only, as shown in Fig. 3 in conductance \( G \) versus susceptance \( B \) plot.

The analysis shows that the active properties of the diode practically are not displayed for more or less significant width of the region \( v \). The solid line of this figure gives dependency for drift layer width \( W_v = 0.6 \mu m \) and the dash line for \( W_v = 1.5 \mu m \). First dependency displays the diode active properties for one narrow frequency band from 50 GHz up to 85 GHz.

Second admittance dependency for \( W_v = 1.5 \mu m \) gives very narrow one frequency band from 40 GHz up to 62 GHz with a very small value of negative conductance \( G \). In general the admittance behavior has a damp oscillation character but only first peak lies in the negative semi plane. The negative conductance disappears completely for \( W_v > 1.5 \mu m \). The main reason of this effect is a non-synchronize mechanism of carriers’ movement along the drift region. This conclusion is contrary to results of the paper [6]. Our results display the active features of the DAR diode the same profile for some frequency bands in case when the \( v \)-region width less than 0.5 \( \mu m \) only.

The DAR diode internal structure optimization has been provided for the second frequency band near 220 GHz for the feeding current density 30 kA/cm\(^2\). The cost function of the optimization process was selected as output power level for the frequency 220 GHz. The set of the variables for the optimization procedure was composed from five technological parameters of the diode structure: two doping levels for \( p \) and \( n \) regions and three widths of \( p, n \) and \( v \) regions. The optimal values of these parameters were found: doping levels of \( n \) and \( p \) zone are equal to \( 0.42 \times 10^{17} \) cm\(^{-3}\) and \( 0.28 \times 10^{17} \) cm\(^{-3}\) respectively, the widths of the two corresponding areas are equal to 0.1 \( \mu m \) and 0.2 \( \mu m \), and the width of the drift \( v \)-region is equal to 0.34 \( \mu m \). The results of the complete analysis for three current density values 30, 50 and 70 kA/cm\(^2\) are shown in Fig.4.
Figure 5. Conductance $G$ dependency as functions of first harmonic amplitude $U_1$ for 220 GHz.

Figure 6. Output generated power $P$ dependency as functions of first harmonic amplitude $U_1$ for 220 GHz.

larger current value provides larger negative conductance and power, but for more heavy temperature regime till the destruction of semiconductor structure. At the same time the characteristics for 50 and 70 kA/cm² are sharper (Fig. 5) because the diode structure was optimized for 30 kA/cm². Nevertheless it is possible to increase the diode active properties for 50 and 70 kA/cm² by means of the structure optimization for these current densities with an additional involving of a special temperature stabilizer.

We can state that a sufficient improvement of power characteristics is observed for this diode structure in comparison with non optimized structure. The maximum values of generated power are equal to 0.33 kW/cm² for $J_0=30$ kA/cm², 0.6 kW/cm² for $J_0=50$ kA/cm² and 0.75 kW/cm² for $J_0=70$ kA/cm² respectively. We can state also that the obtained results are the best for the defined frequency band; but can be improved for other current density.

5. CONCLUSIONS

The numerical scheme that has been developed for the analysis of the different types of IMPATT diodes is suitable for the DAR complex doping profile investigation too. The additional problem that appears for the DAR diode structure analysis is the slower convergence of the numerical model in comparison with the DDR diode analysis for instance.

Some new features of the DAR diode were obtained by the analysis on the basis of nonlinear model. The principal obtained results contradict to the data that were obtained before on the basis of the approximate models of the DAR diode. These results show that the diode does not have the active properties in some frequency bands for the sufficiently large drift region. To obtain the negative conductance for some frequency bands we need to reduce the drift layer widths to obtain $W_v$ lesser than 0.5 $\mu$m. Nevertheless the diode has a wide first frequency band and two superior frequency bands with sufficient output power level. The diode structure optimization gives the possibility to increase the output power level for high frequency bands. This level can be exceeding by the special diode structure optimization taking into account necessary feeding current density.

6. ACKNOWLEDGEMENT

This work was supported by the Mexican National Council of Science and Technology CONACYT, under project SEP-2004-C01-46510.

7. REFERENCES


